



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#4 2661

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Applicants: Blightman, et al.
Assignee: Alacritech, Inc.
Title: "REDUCING DELAYS ASSOCIATED WITH INSERTING A CHECKSUM INTO A NETWORK MESSAGE"
Serial No.: 09/802,426 Filed: March 09, 2001
Examiner: Unknown Tel:
Docket No.: ALA-014 Art Unit: 2661

Box DD
Assistant Commissioner For Patents
Washington, D.C. 20231

June 29, 2001

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §1.56, §1.97 and §1.98, Applicants bring the 84 documents listed on the enclosed forms PTO-1449 to the Examiner's attention in the above-captioned application. Citation of the listed documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant application;
2. a representation that a search has been made; or
3. an admission that the information cited is, or is considered to be, material to patentability as defined in §1.56(b).

Only 26 of the 84 documents listed on the enclosed PTO-1449 forms are provided. The remaining documents were either cited by the Examiner or submitted by Applicants in prior applications (09/464,283, 09/439,603, 60/061,809, 09/384,792, 60/098,296 or 09/067,544) that are relied upon for an earlier filing date under 35 U.S.C. §120.

I hereby certify that this correspondence is being deposited with the United States Postal Service as "First Class Mail to Addressee" on the date indicated below and is addressed to: Box DD, Assistant Commissioner for Patents, Washington, D.C. 20231, on June 29, 2001.

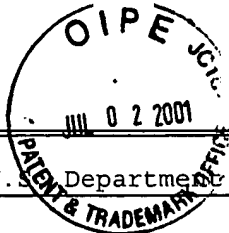
June 29, 2001
Date

Lester Wallace
T. Lester Wallace

Respectfully submitted,

Lester Wallace

T. Lester Wallace
Attorney for Applicants
Reg. No. 34,748



U.S. Department of Commerce, Patent and Trademark Office	Application No.: 09/802,426
	Filing date: March 09, 2001
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Inventors: Blightman, et al.
	Group Art Unit: 2661
''REDUCING DELAYS ASSOCIATED WITH INSERTING A CHECKSUM INTO A NETWORK MESSAGE''	Examiner name: Unknown
	Attorney Docket No. ALA-014

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	AA	5,280,477	1/18/94	Trapp	370	85.1	
	AB	5,448,566	9/5/95	Richter, et al.	370	94.1	
	AC	5,634,127	5/27/97	Cloud, et al.	395	680	
	AD	5,751,715	5/12/98	Chan, et al.	370	455	
	AE	4,991,133	2/5/91	Davis, et al.	364	900	
	AF	5,163,131	11/10/92	Row, et al.	395	200	
	AG	5,212,778	5/18/93	Dally, et al.	395	400	
	AH	5,289,580	2/22/94	Latif, et al.	395	275	
	AI	5,303,344	4/12/94	Yokoyama, et al.	395	200	
	AJ	5,412,782	5/2/95	Hausman, et al.	395	250	
	AK	5,485,579	1/16/96	Hitz, et al.	395	200.12	
	AL	5,506,966	4/9/96	Ban	395	250	
	AM	5,511,169	4/23/96	Suda	395	280	
	AN	5,548,730	8/20/96	Young, et al.	395	280	

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Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
	AO	PCT/US98/08719 (WO98/50852)	11/12/98	International				
	AP	PCT/US97/17257 (WO98/19412)	5/7/98	International				

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	AQ	Internet pages entitled: Technical White Paper - Xpoint's Disk-to-LAN Acceleration Solution for Windows NT Server, printed 6/5/97.

Examiner

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*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	AR	5,588,121	12/24/96	Reddin, et al.	395	200.15	
	AS	5,590,328	12/31/96	Seno, et al.	395	675	
	AT	5,634,099	5/27/97	Andrews, et al.	395	200.07	
	AU	5,642,482	6/24/97	Pardillos	395	200.2	
	AV	5,671,355	9/23/97	Collins	395	200.2	
	AW	5,692,130	11/25/97	Shobu, et al.	395	200.12	
	AX	5,701,434	12/23/97	Nakagawa	395	484	
	AY	5,749,095	5/5/98	Hagersten	711	141	
	AZ	5,752,078	5/12/98	Delp, et al.	395	827	
	BA	5,758,084	5/26/98	Silverstein, et al.	395	200.58	
	BB	5,758,089	5/26/98	Gentry, et al.	395	200.64	
	BC	5,758,186	5/26/98	Hamilton, et al.	395	831	
	BD	5,758,194	5/26/98	Kuzma	395	886	

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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

BE	Jato Technologies Internet pages entitled: Network Accelerator Chip Architecture (twelve-slide presentation), 13 pages, printed 8/19/98.
BF	EETIMES article entitled: Enterprise system uses flexible spec, by Christopher Harrer and Pauline Shulman, dated August 10, 1998, Issue 1020. 3 pages, printed 11/25/98.
BG	Internet pages entitled: iReady About Us and iReady Products. 5 pages, printed 11/25/98.
BH	Internet pages entitled: Smart Ethernet Network Interface Card (which Berend Ozceri is developing). 2 pages, printed 11/25/98.
BI	Internet pages entitled : Hardware Assisted Protocol Processing (which Eugene Feinberg is working on). 1 page, printed 11/25/98.
BJ	Internet pages of XaQti Corporation entitled: Giga POWER Protocol Processor Product Preview. 4 pages, printed 11/25/98.
BK	"Toshiba Delivers First Chips to Make Consumer Devices Internet-Ready Based on iReady Design," Press Release October 14, 1998. 3 pages. (printed 11/28/98).
BL	Internet pages from website http://www.ireadyco.com/about.html , 3 pages, downloaded 11/2/98.

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U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	BM	5,566,170	10/15/96	Bakke, et al.	370	60	
	BN	6,009,478	12/28/99	Panner, et al.	710	5	
	BO	6,034,963	3/07/00	Minami, et al.	370	401	
	BP	5,878,225	03/02/99	Bilansky et al.	395	200.57	
	BQ	5,991,299	11/23/99	Radogna et al.	370	392	
	BR	5,930,830	07/27/99	Mendelson et al.	711	171	
	BS	6,061,368	05/09/00	Hitzelberger	370	537	
	BT	5,056,058	10/8/91	Hirata, et al.	364	900	
	BU	5,678,060	10/14/97	Yokoyama et al.	395	831	

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		Document Number	Date	Country	Class	Subclass	Yes	No
	BV	PCT/US98/14729 (WO99/04343)	1/28/99	International				
	BW	PCT/US98/24943 (WO 00/13091)	03/09/00	International				
	BX	PCT/US99/13184 (WO 99/65219)	06/20/99	International				

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	BY	60/053,240 (U.S. Provisional Application), by Jolitz et al. (listed filing date July 18, 1997).
	BZ	Zilog Product Brief entitled "Z85C30 CMOS SCC Serial Communication Controller", Zilog Inc., 3 pages (1997).
	CA	IReady News Archive, "Revolutionary Approach to Consumer Electronics Internet Connectivity Funded," San Jose, CA November 20, 1997. 2 pages, printed 11/2/98.
	CB	IReady News Archive, "Seiko Instruments Inc. (SII) INTRODUCES WORLD'S FIRST INTERNET-READY INTELLIGENT LCD MODULES BASED ON IREADY TECHNOLOGY," Santa Clara, CA and Chiba, Japan, October 26, 1998. 2 pages, printed 11/2/98.

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U.S. Department of Commerce, Patent and Trademark Office	Application No.: 09/802,426
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INFORMATIONAL DISCLOSURE STATEMENT BY APPLICANT	Inventors: Blightman, et al.
	Group Art Unit: 2661
"REDUCING DELAYS ASSOCIATED WITH INSERTING A CHECKSUM INTO A NETWORK MESSAGE"	Examiner name: Unknown.
	Attorney Docket No. ALA-01

OTHER ART (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

CC	Internet pages of Xpoint Technologies www.xpoint.com website, 5 pages, printed 12/19/97.
CD	Internet pages entitled: Asante and 100BASE-T Fast Ethernet. 7 pages, printed 5/27/97.
CE	Internet pages entitled: A Guide to the Paragon XP/S-A7 Supercomputer at Indiana University. 13 pages, printed 12/21/98.
CF	Richard Stevens, "TCP/IP Illustrated, Volume 1, The Protocols", pages 325-326 (1994).
CG	VT8501 Apollo MVP4 Documentation, VIA Technologies, Inc., pages i-iv, 1-11, cover and copyright page, revision 1.3 (Feb. 1, 2000).
CH	Internet pages entitled: Northridge/Southbridge vs. Intel Hub Architecture, 4 pages, printed 2/19/01.
CI	Gigabit Ethernet Technical Brief, Achieving End-to-End Performance. Alteon Networks, Inc., First Edition, September 1996.
CJ	Internet pages directed to; Technical Brief on Alteon Ethernet Gigabit NIC technology, www.alteon.com , 14 pages, printed 3/15/97.
CK	Internet pages from IReady News Archives entitled, "iReady Rounding Out management team with two key executives," 2 pages, downloaded 11/28/98.
CL	"Toshiba Delivers First Chips to Make Consumer Devices Internet-Ready Based On iReady's Design," Press Release October, 1998. 3 pages, downloaded 11/28/98.
CM	Internet pages from iReady Products, web site http://www.ireadyco.com/products.html , 2 pages, downloaded 11/25/98.
CN	IReady News Archives, "Toshiba, iReady shipping Internet chip," 1 page, posted at 9:39 AM PT, Oct. 14, 1998.
CO	iReady Corporation, The i-1000 Internet Tuner, Modular Internet Design System, 2 pages, date unknown.
CP	Interprophet website, http://www.interprophet.com/technology.html , 17 pages, downloaded 3/1/00.
CQ	NEWSwatch - IReady Internet Tuner to Web Enable Devices, November 5, 1996. 2 pages, printed 11/2/98.
CR	David Lammers, EETimes, 06/13/97, Tuner for Toshiba, Toshiba taps iReady for Internet tuner. 3 pages, printed 11/02/98.
CS	Internet pages entitled: DART Fast Application - Level Networking Via Data-Copy Avoidance, by Robert J. Walsh, 25 pages. Printed 6/3/99.
CT	Internet pages of InterProphet entitled: Frequently Asked Questions, by Lynne Jolitz, 4 pages. Printed 6/14/99.

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	CU	4,336,538	12/28/92	Johnson et al.	364	200	
	CV	5,097,442	03/17/92	Ward et al.	365	78	
	CW	5,592,622	01/07/97	Isfeld et al.	395	200.02	
	CX	5,629,933	05/13/97	Delp et al.	370	411	
	CY	5,664,114	09/02/97	Krech, Jr. et al.	395	200.64	
	CZ	5,699,317	12/16/97	Sartore et al.	365	230.06	
	DA	5,771,349	06/23/98	Picazo, Jr. et al.	395	188.01	
	DB	5,790,804	08/04/98	Osborne	395	200.75	
	DC	5,794,061	08/11/98	Hansen et al.	395	800.01	
	DD	5,812,775	09/22/98	Seeters et al.	395	200.43	
	DE	5,802,580	09/01/98	McAlpine	711	149	
	DF	5,815,646	09/29/98	Purcell et al.	395	163	

Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
	DG							
	DH							
	DI							

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